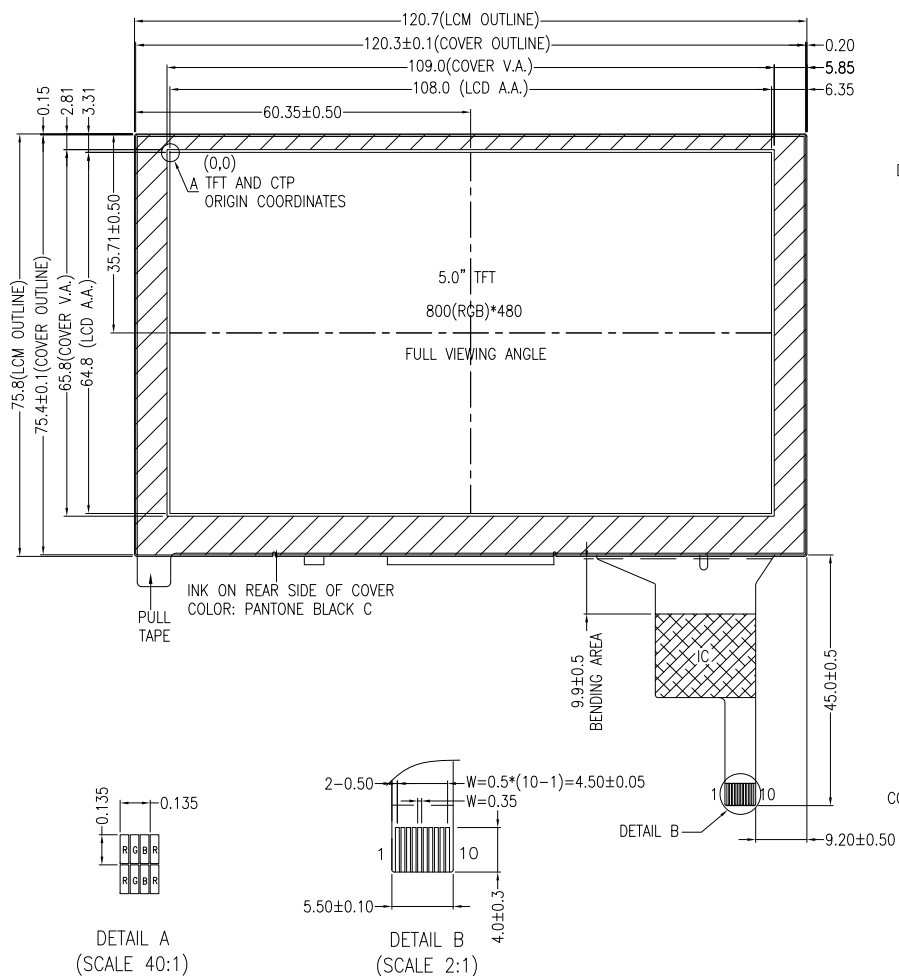


NO MECHANICAL HOUSING TO HOLD THE TFT AND THE TOUCH PANEL ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE TOUCH PANEL.



NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCC=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: SSD1963
5. LED BACKLIGHT: WHITE LED
BUILT-IN BACKLIGHT DRIVER: VCC=3.3V
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE

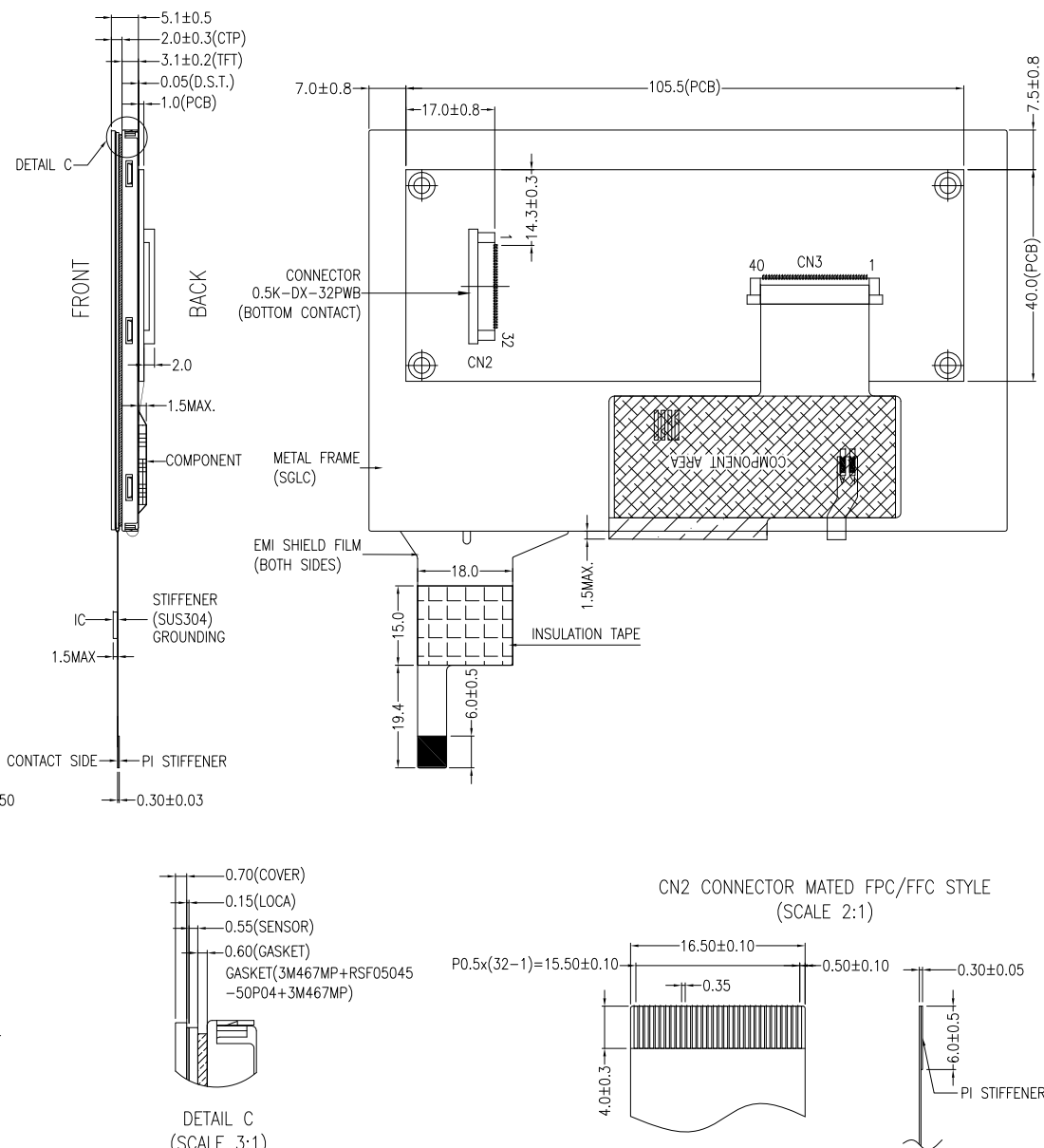
NOTES FOR OTHERS:

1. OPERATING TEMP.: -30°C ~ 85°C
2. STORAGE TEMP.: -30°C ~ 85°C
3. SURFACE LUMINANCE: 510 CD/M² (TYP.) WITH CTP
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE
AND THE MINIMUM BEND RADIUS IS 1.5MM
5. GENERAL TOLERANCE: ±0.3MM
6. ROHS COMPLIANT

NOTES FOR CTP:

1. 5.0 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VDD=3.3V
4. DRIVER IC: FT5446DQS-P03
5. INTERFACE: IIC
6. RESOLUTION: 800*480 DOTS
7. MULTI FINGER: UP TO 5
8. HARDNESS OF COVER SURFACE: 7H
9. FIRMWARE FILE:

MI0500CJP-C6(FT5446DQS-P03) FIRMWARE V0B 20201104 ALL.BIN



PIN ASSIGNMENT FOR CN2	
PIN	SYMBOL
1	GND
2	VCC
3	BL_E
4	RS
5	/WR
6	/RD
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	DB8
16	DB9
17	DB10
18	DB11
19	DB12
20	DB13
21	DB14
22	DB15
23	VCC
24	GND
25	/CS
26	/RST
27	TE
28	NC
29	NC
30	NC
31	NC
32	DISP
PIN ASSIGNMENT FOR CTP	
PIN	SYMBOL
1	VSS
2	VDD
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

 MULTI-INNO TECHNOLOGY CO., LTD.			
DRAWN BY:		RITA	2024.04.01
CHECKED BY:			
APPROVED BY:		SCALE : 1/1	MODULE P/N:
VERSION NO:		UNIT: mm	MI0500AHT-3CP1-B2
01		SHEET NO.: 1/1	DESCRIPTION:
			TFT + CTP + PCBA MODULE